

Fig. 1 Prior Art



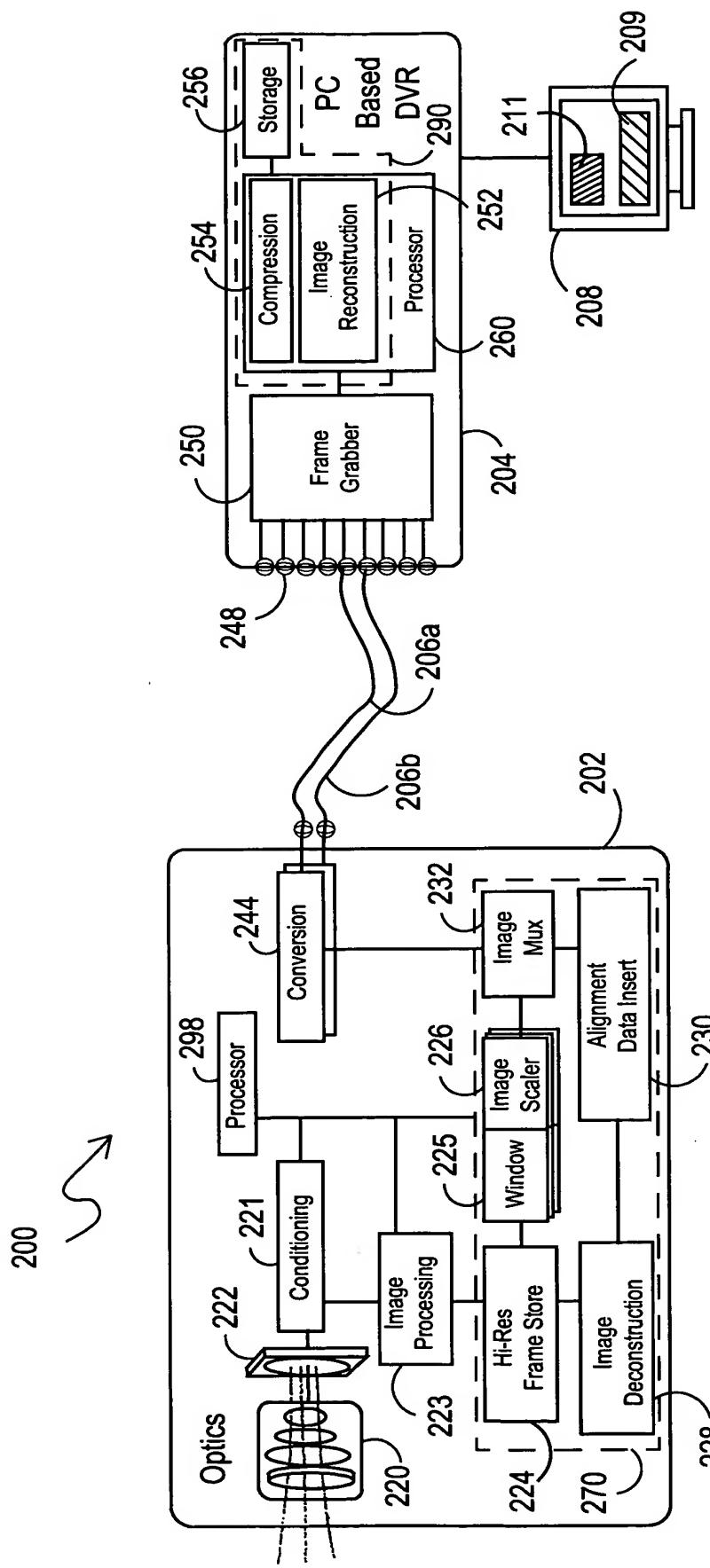


Fig. 2

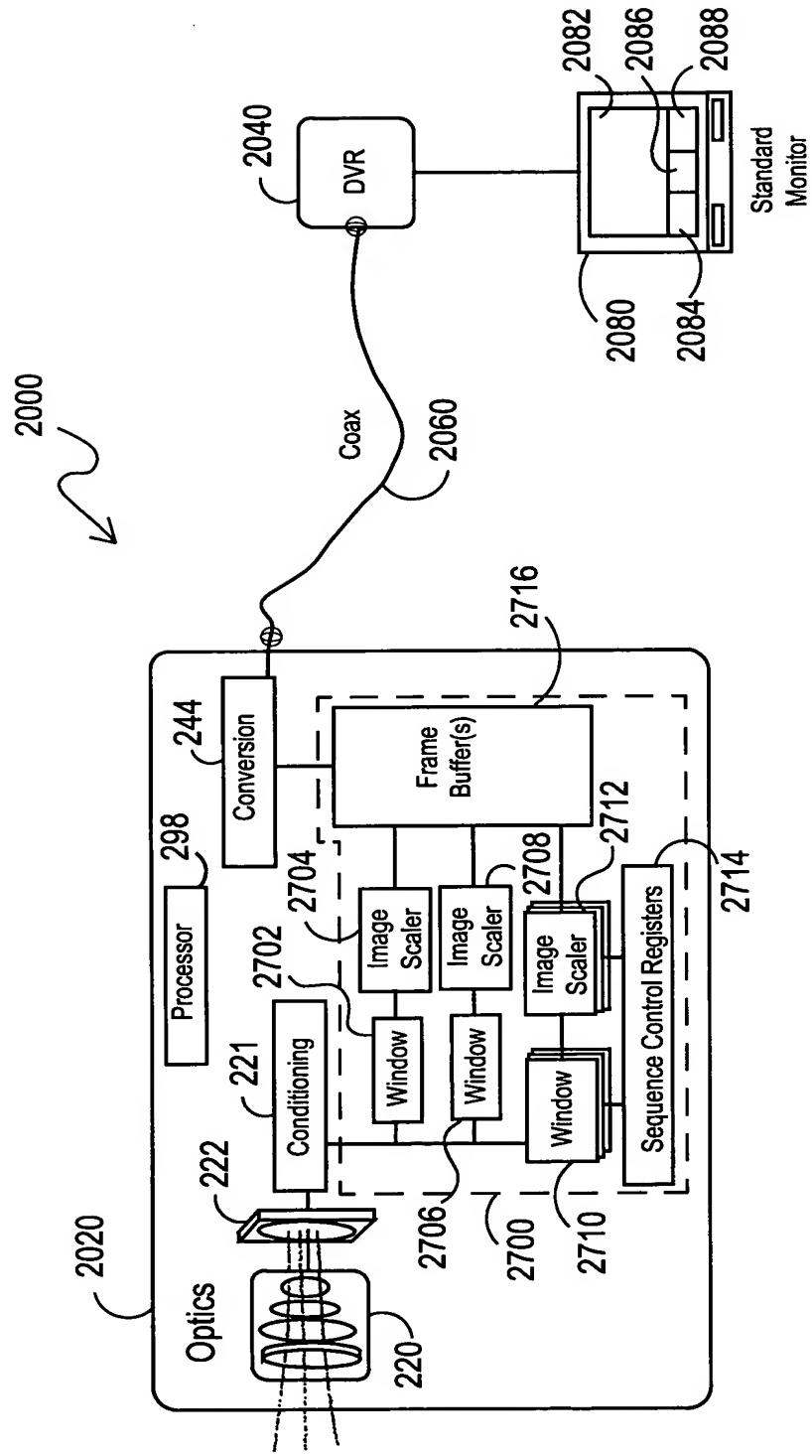


Fig. 3A

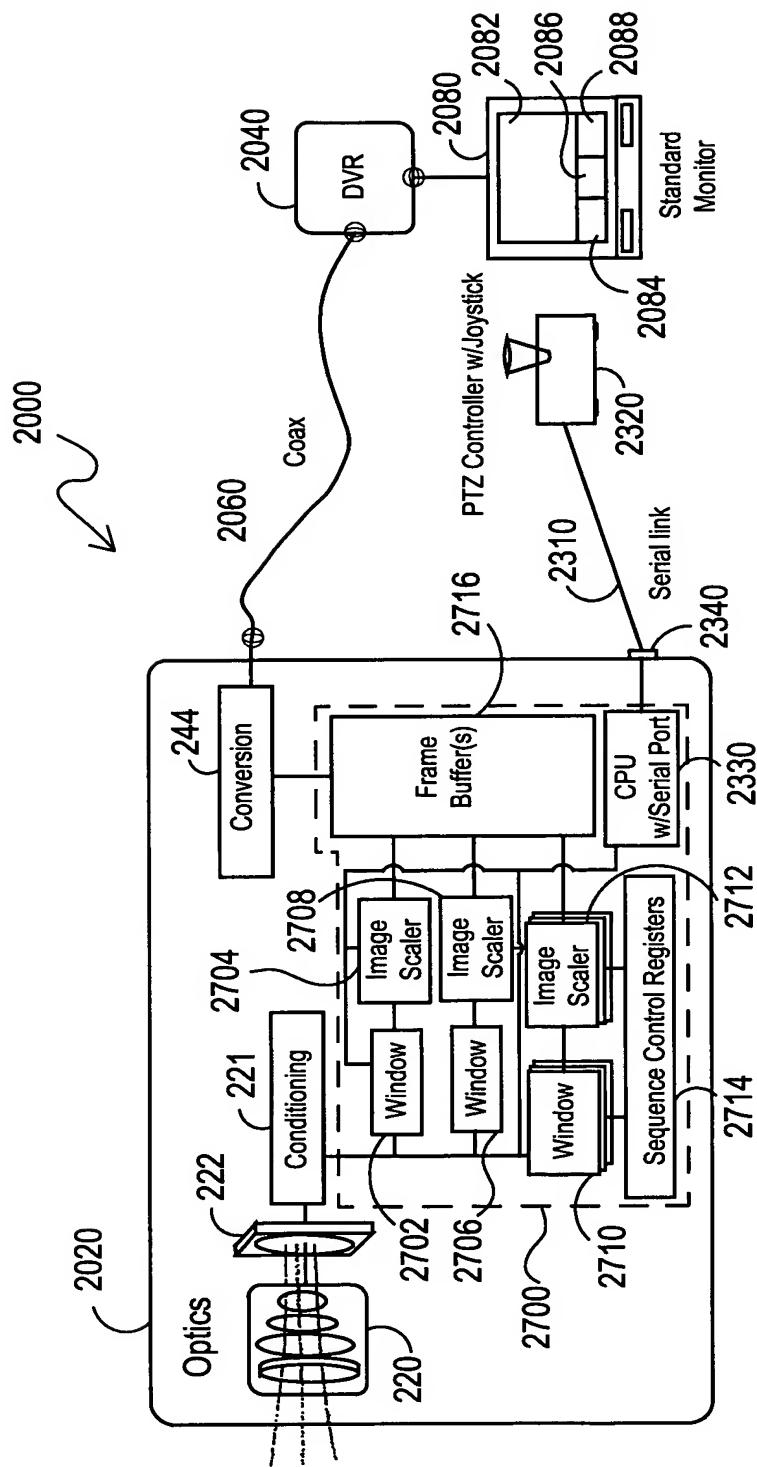


Fig. 3B

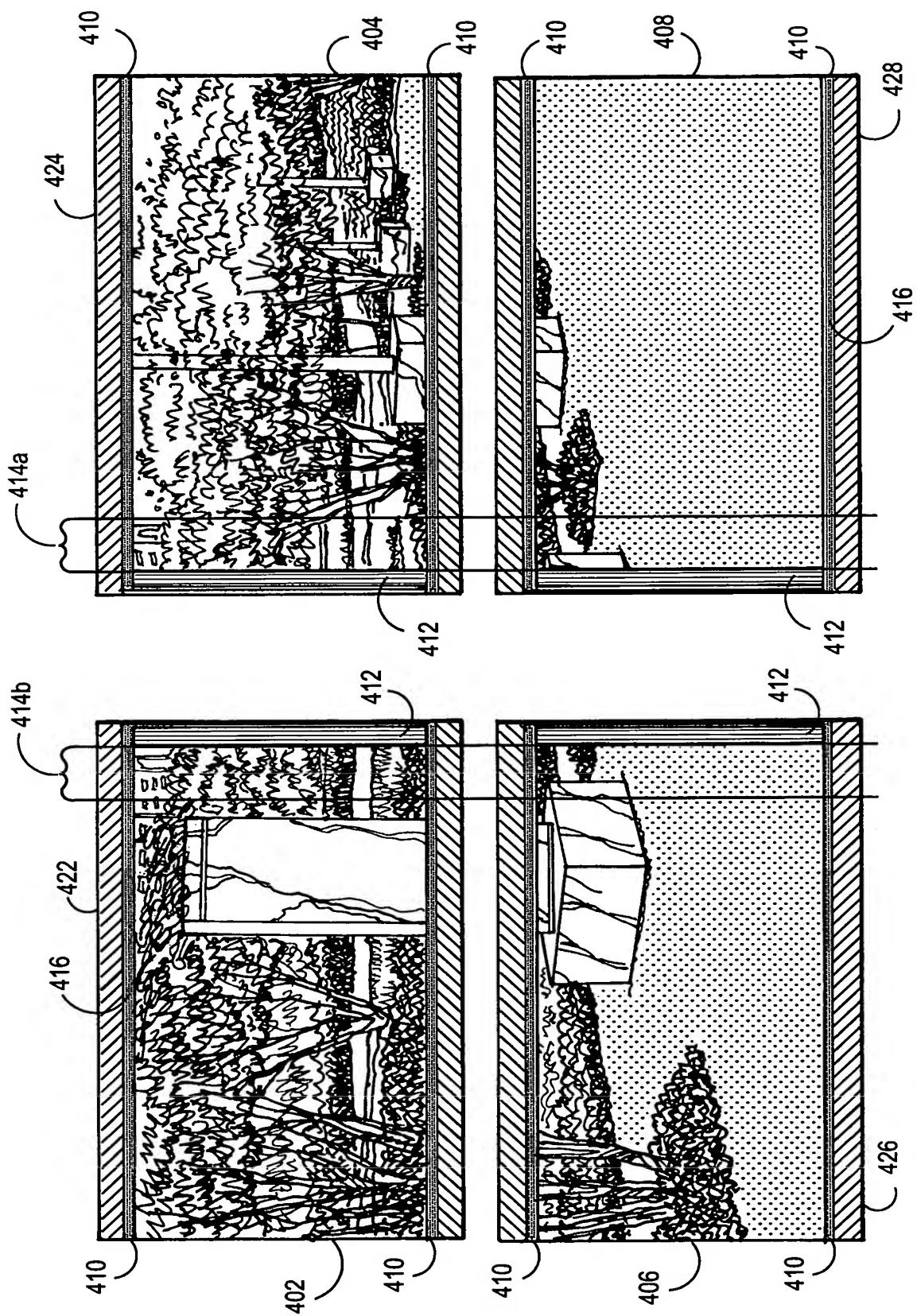


FIG. 4A

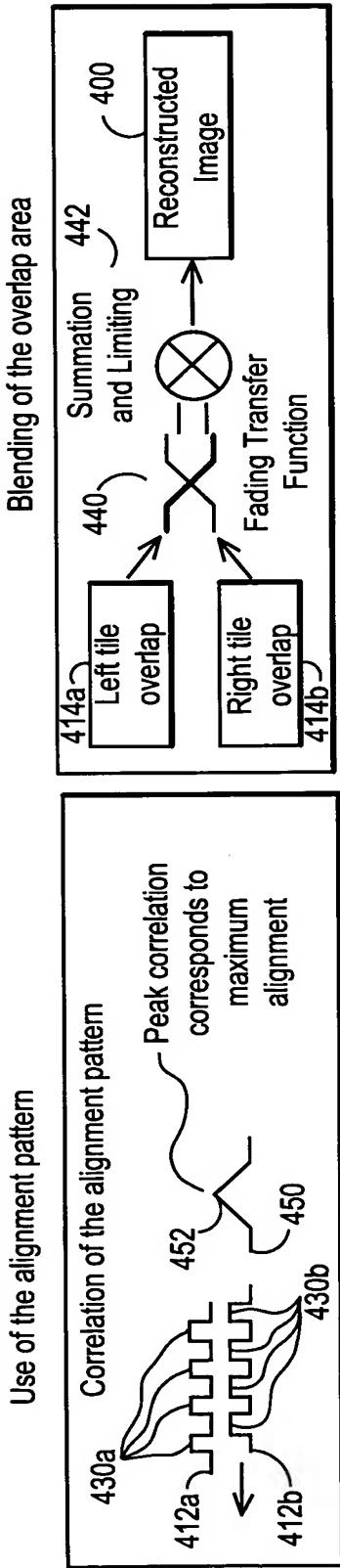


FIG. 4B

FIG. 4C

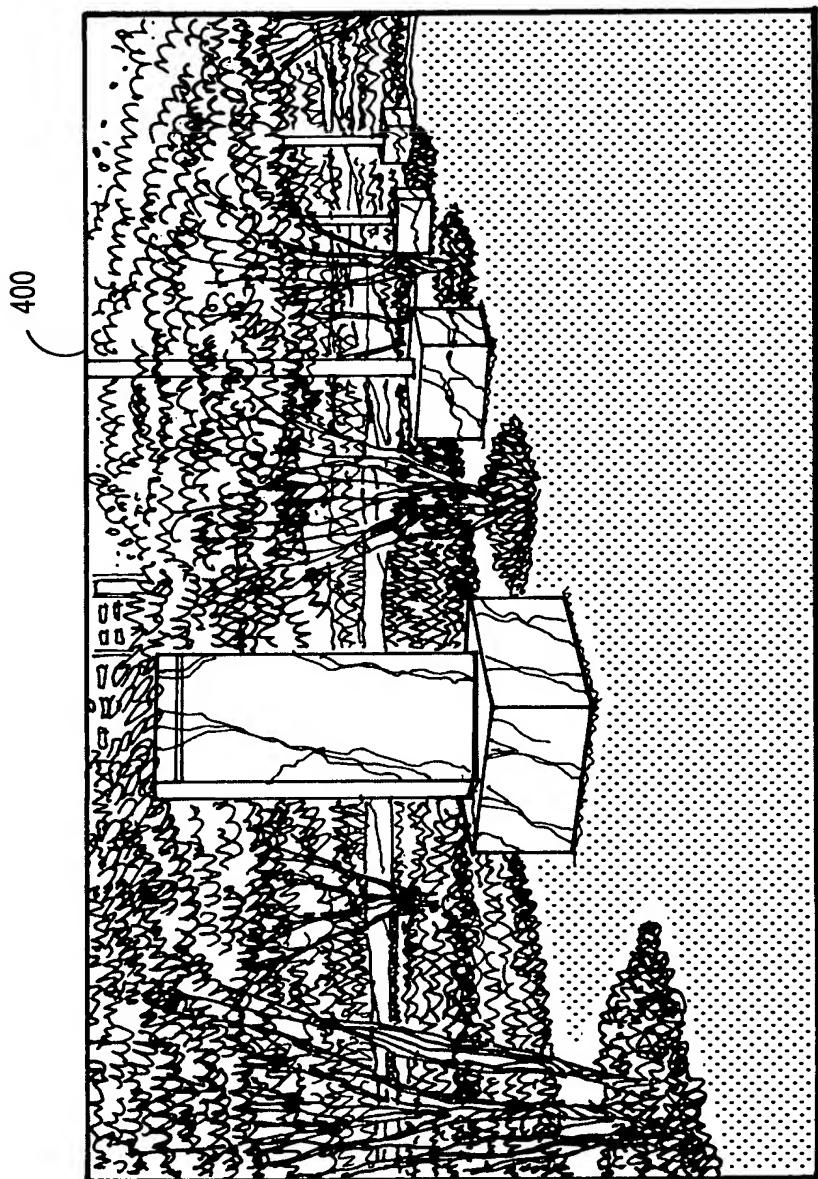


FIG. 4D - Reconstructed Image

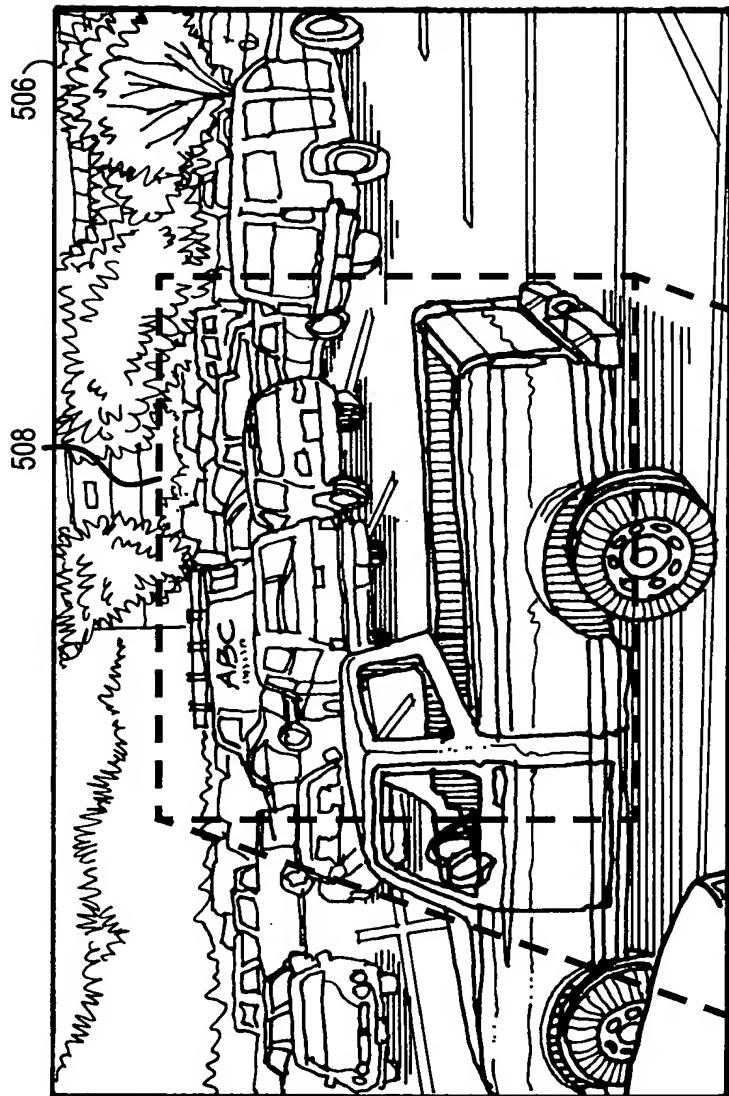


FIG. 5A

Example of extraction of a
640x480 area out of the
1280x720 sensor array

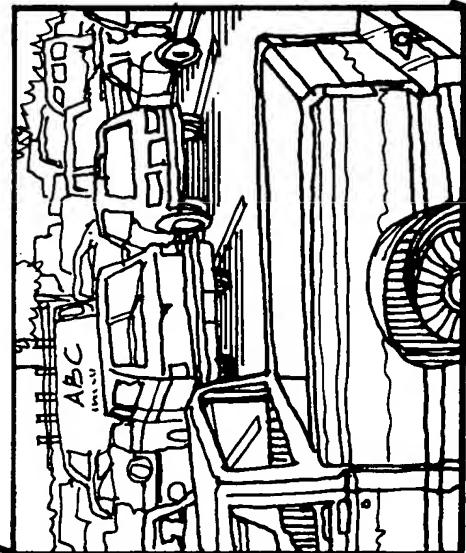


FIG. 5B

510

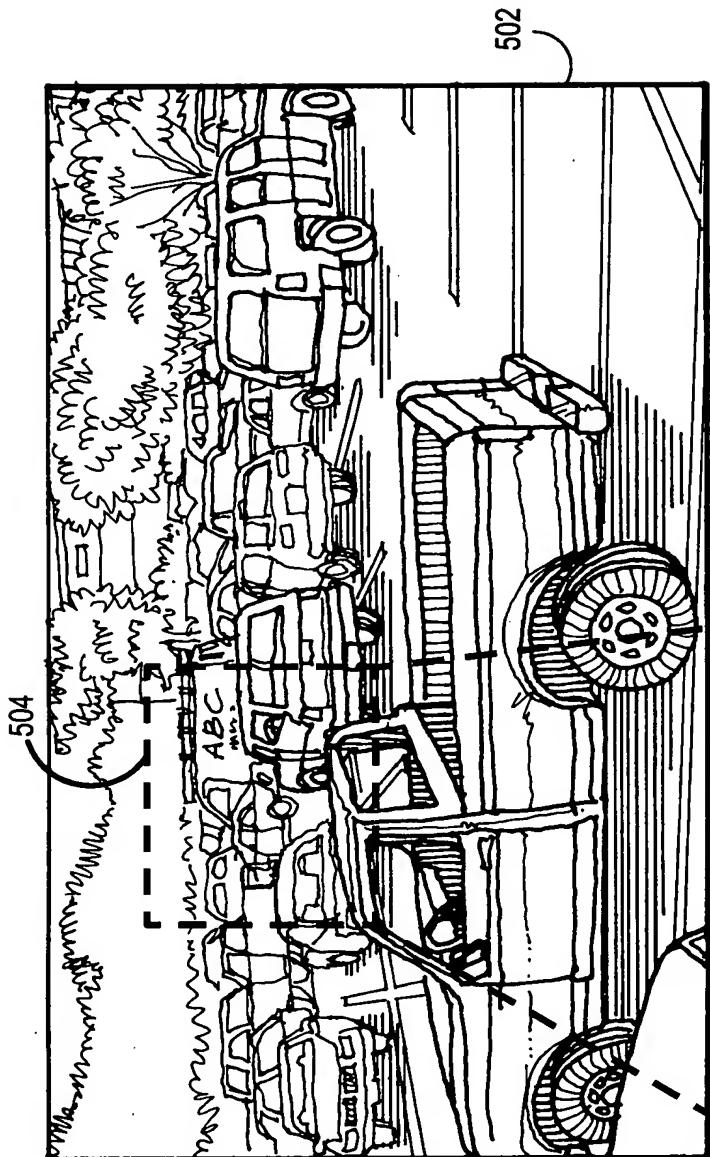
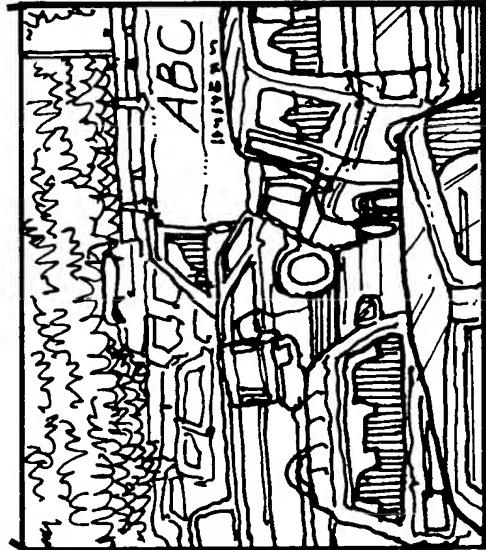


FIG. 5C

Example of extraction of a 320x240 area out of the 1280x720 sensor array and scaling the 320x240 section out to 640x480



550

FIG. 5D



FIG. 6A

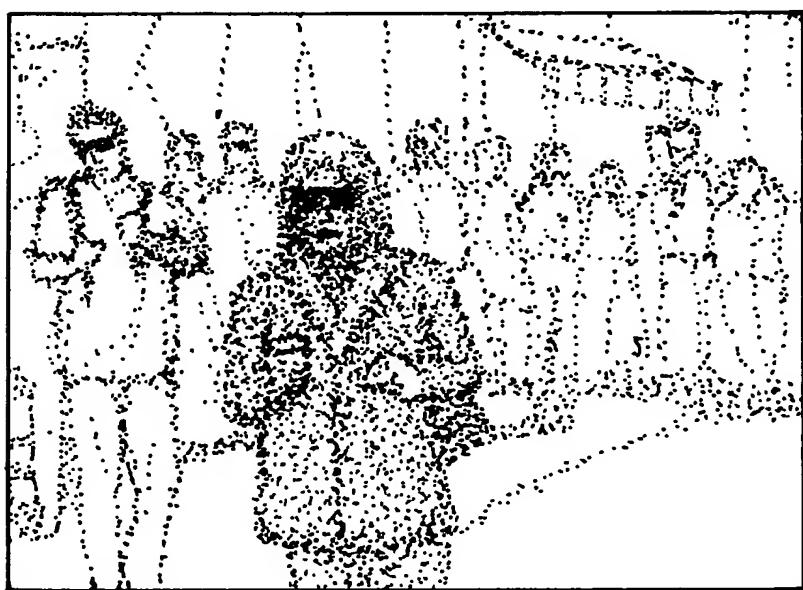


FIG. 6B



FIG. 6C

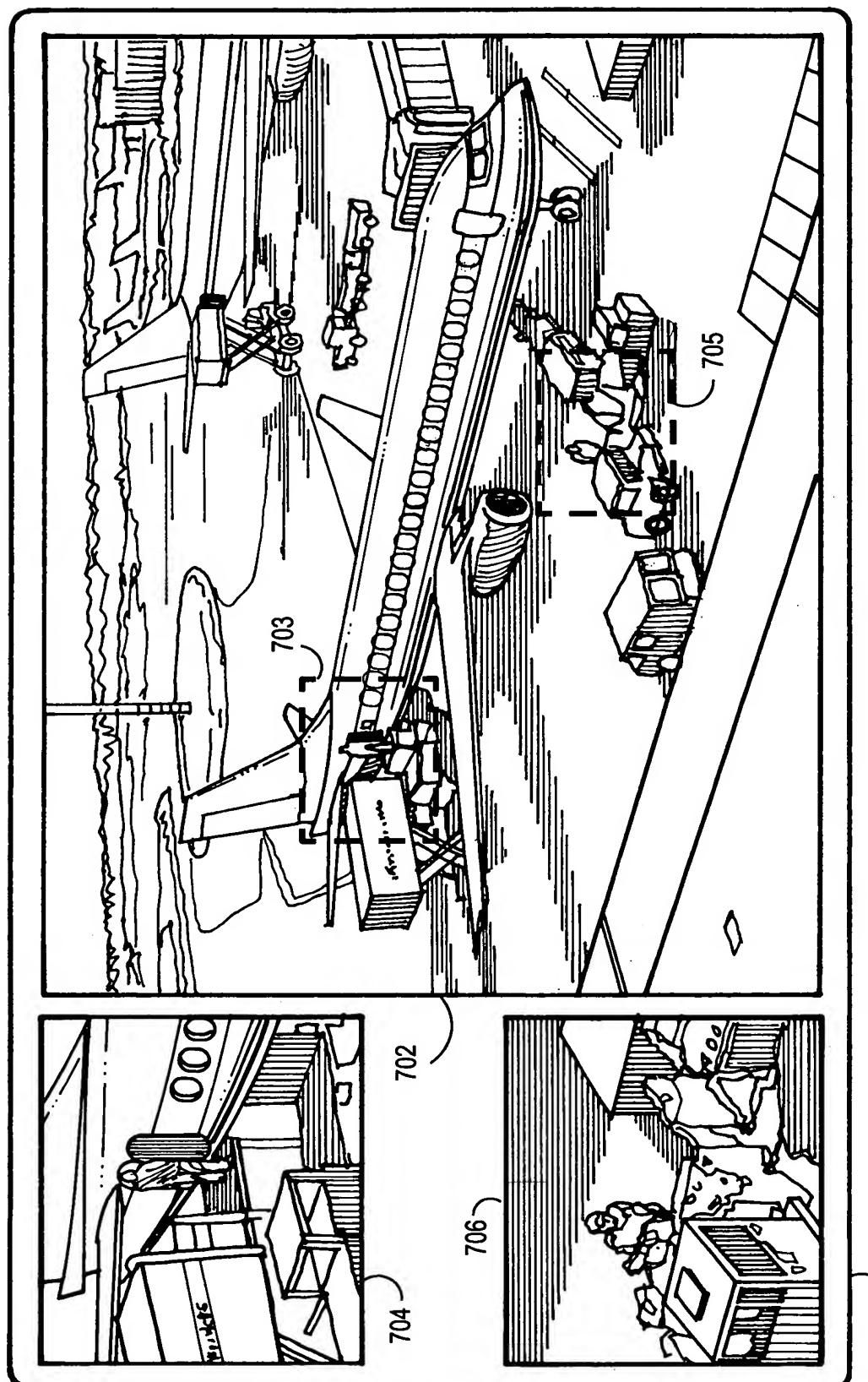


FIG. 7

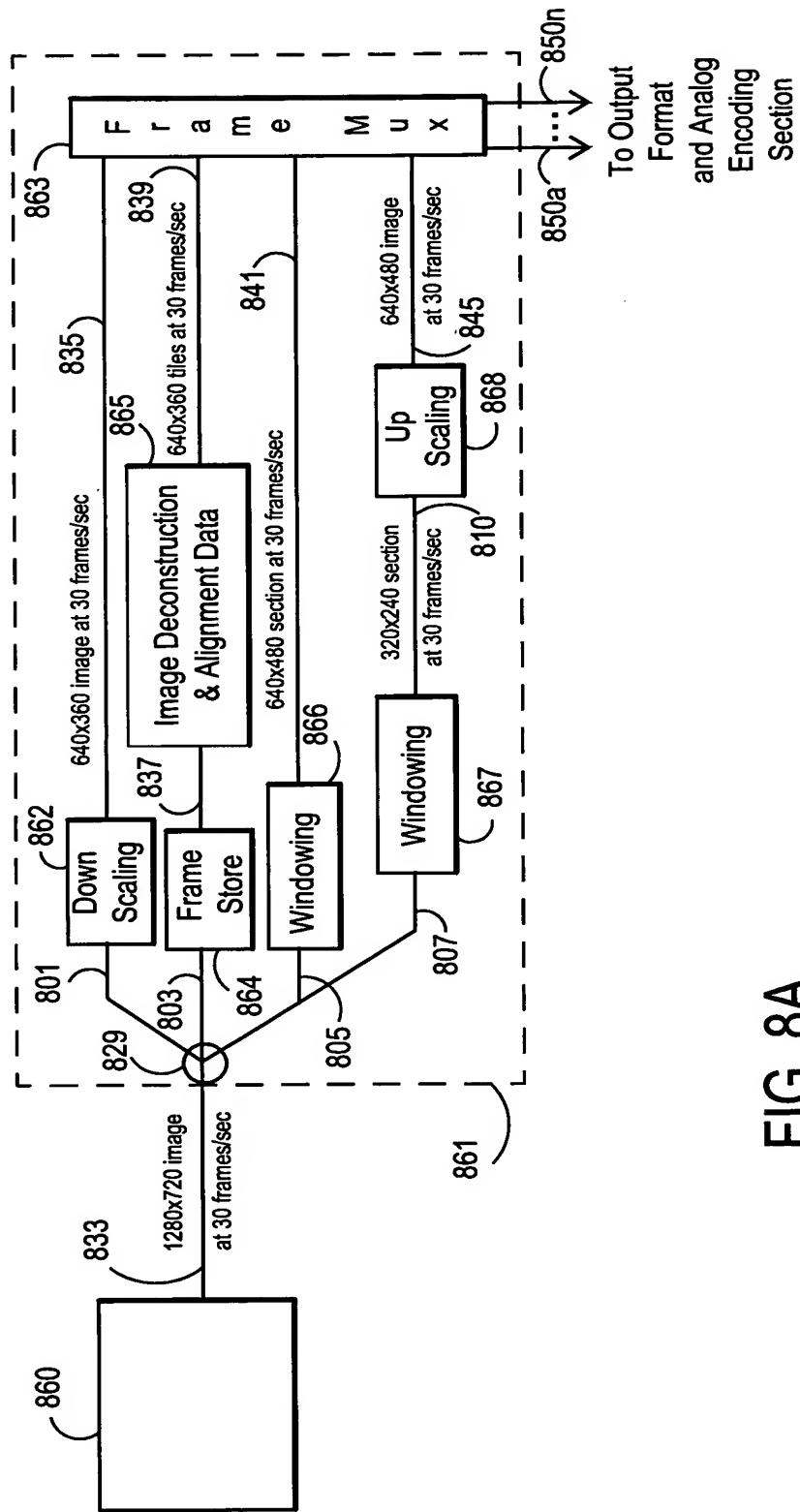


FIG. 8A

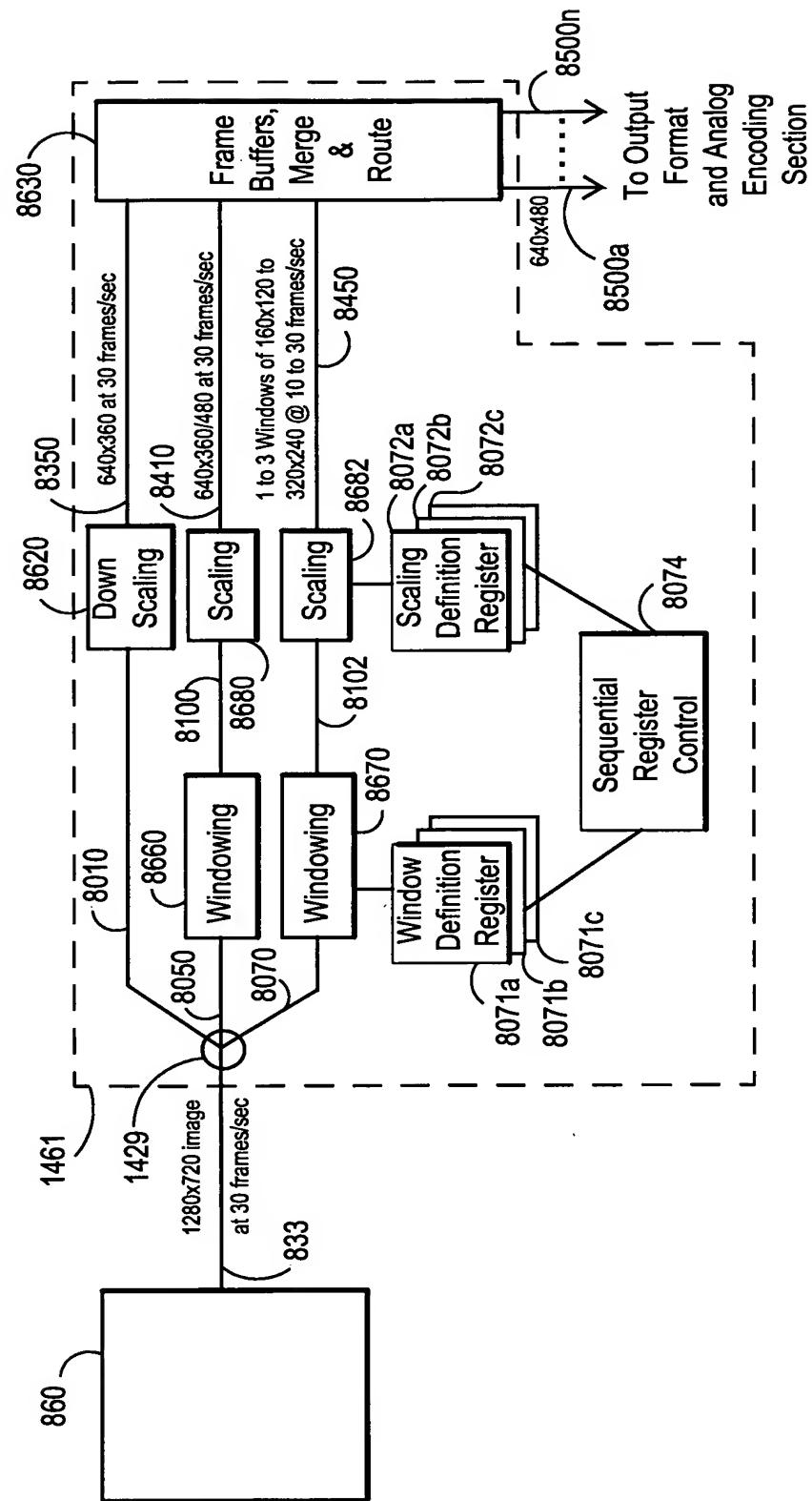


FIG. 8B

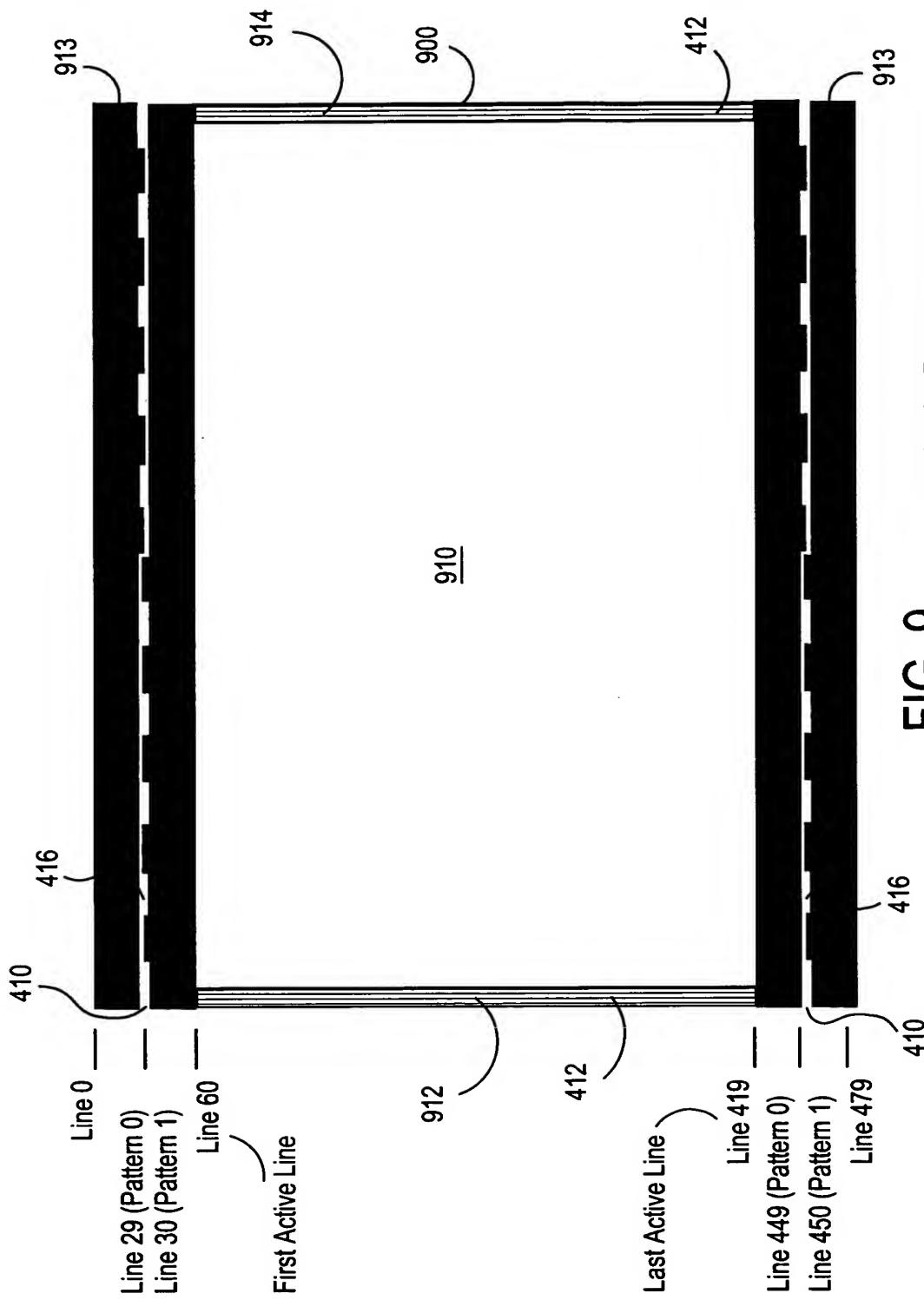


FIG. 9 De-Interlaced Active Frame

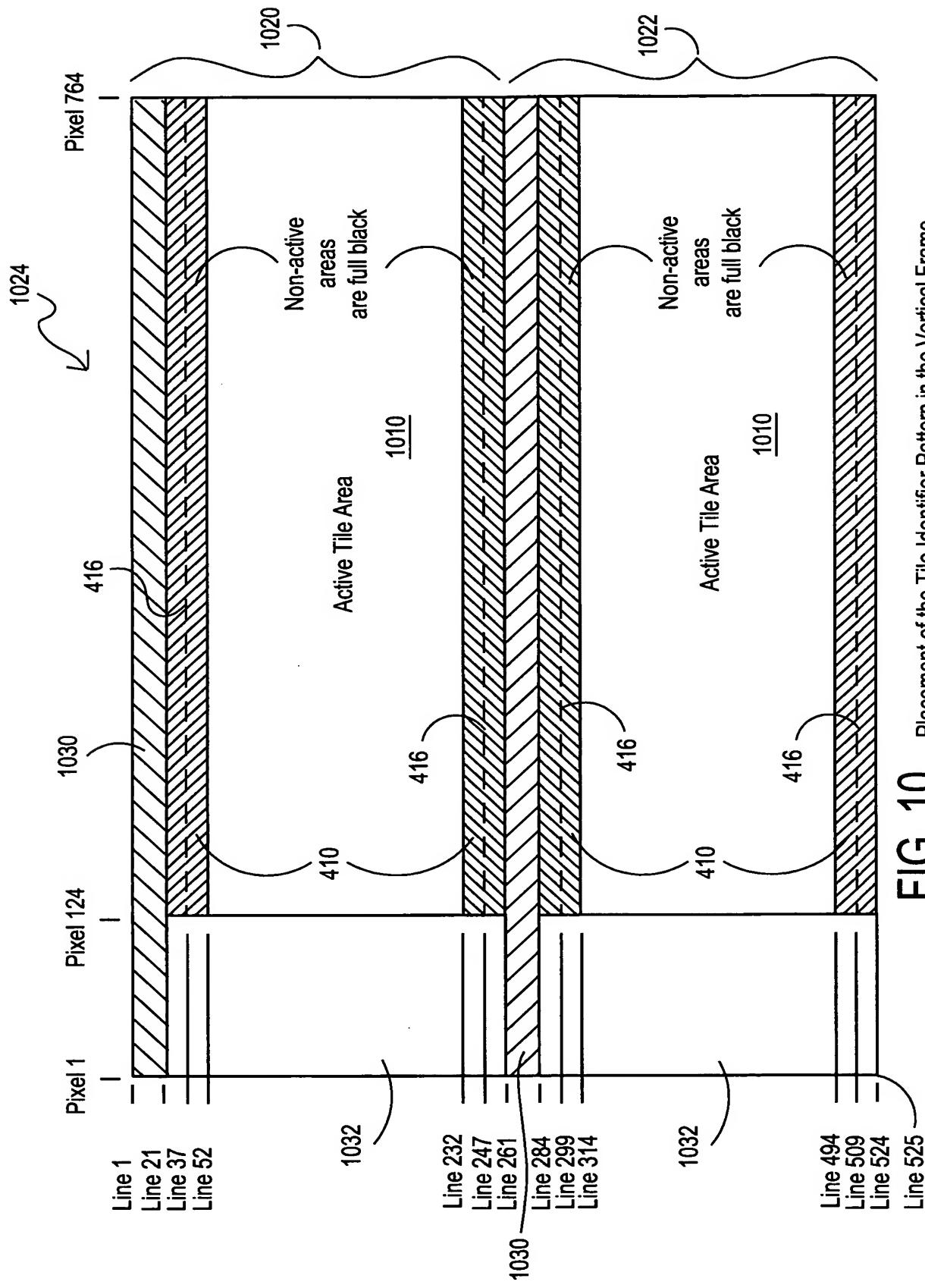


FIG. 10 Placement of the Tile Identifier Pattern in the Vertical Frame

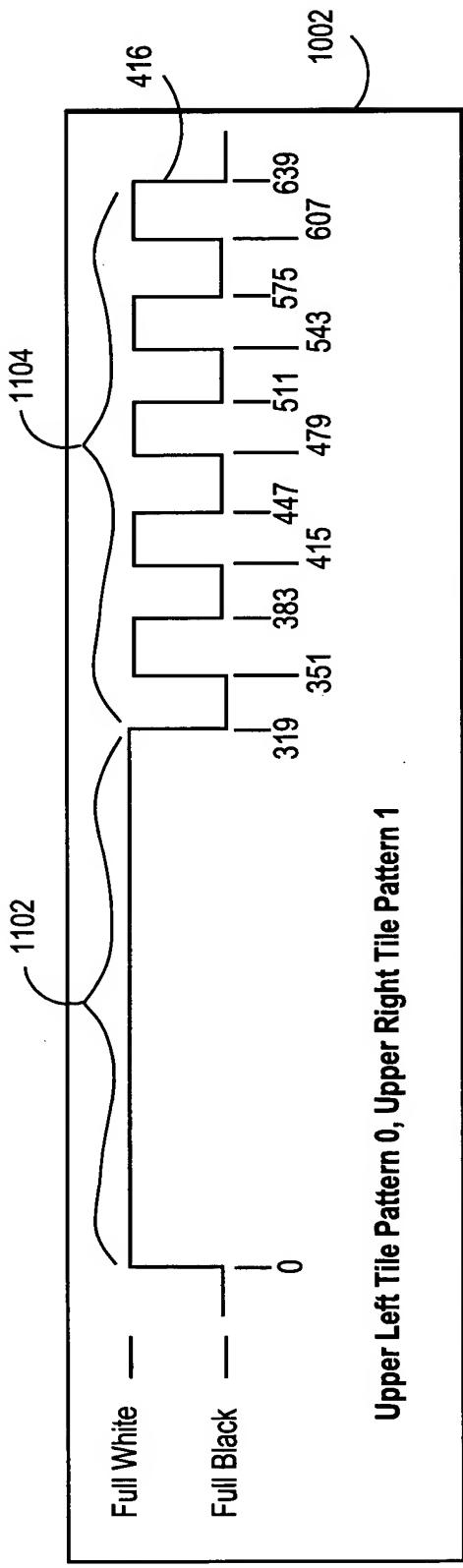


FIG. 11A

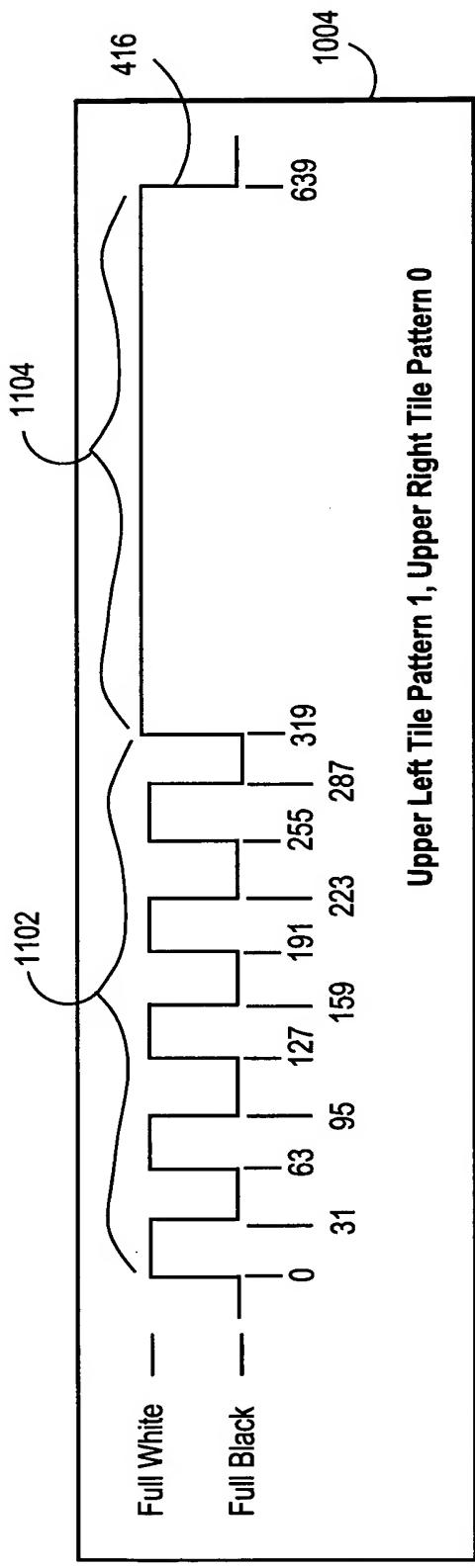


FIG. 11B

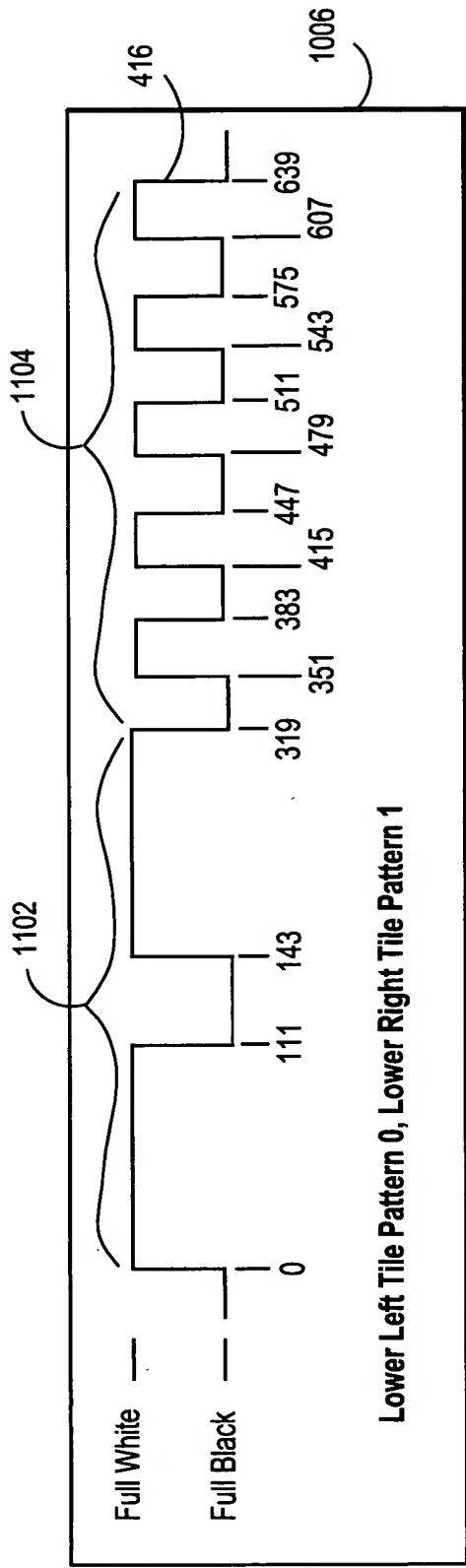
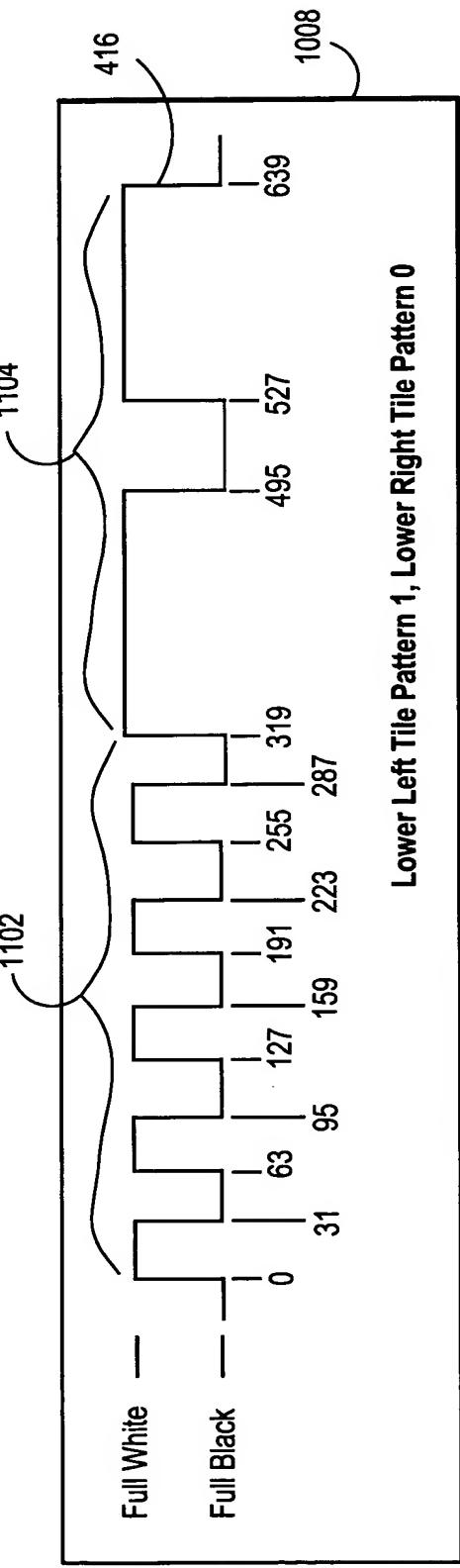


FIG. 11C



Lower Left Tile Pattern 1, Lower Right Tile Pattern 0

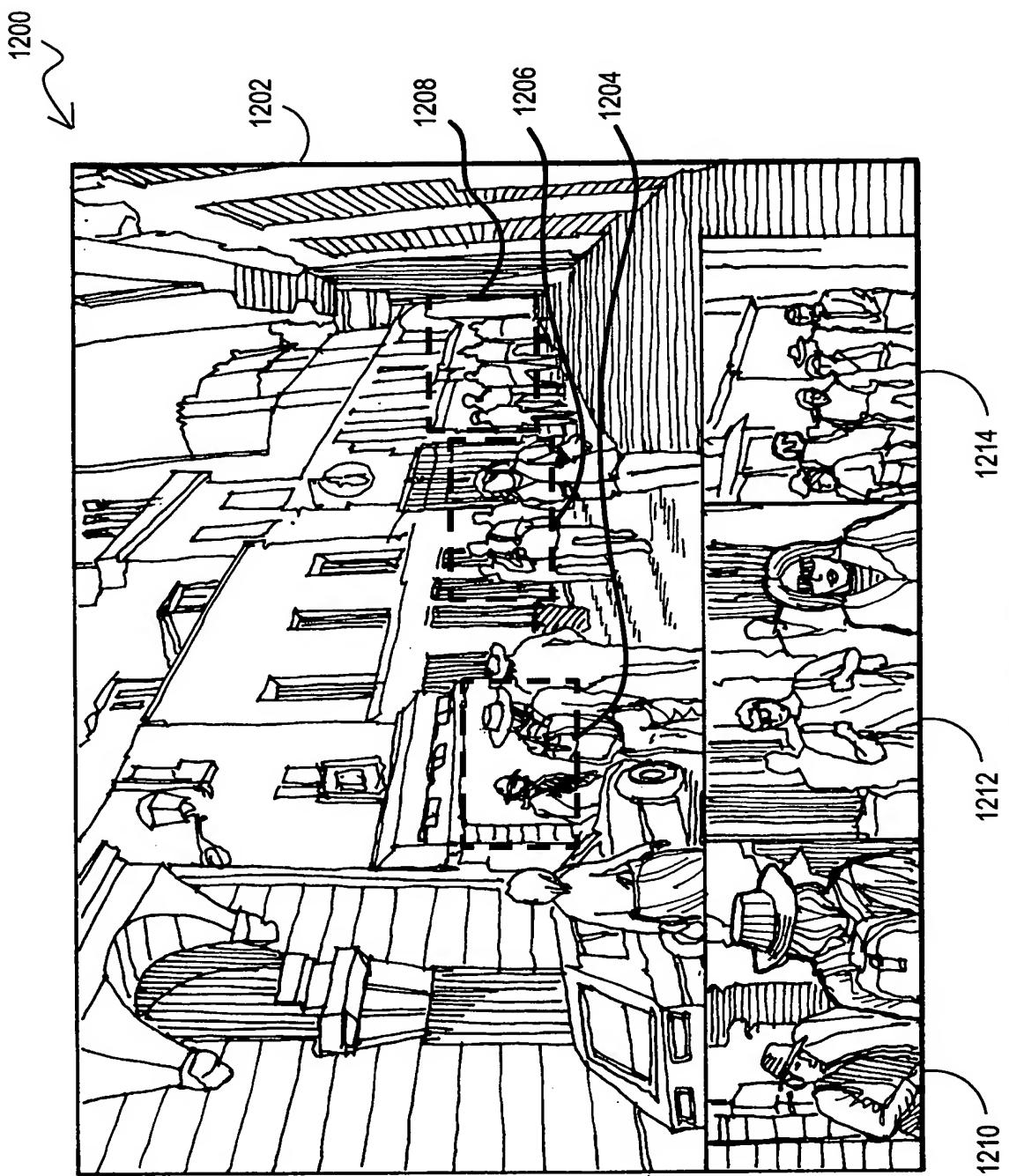


FIG. 12

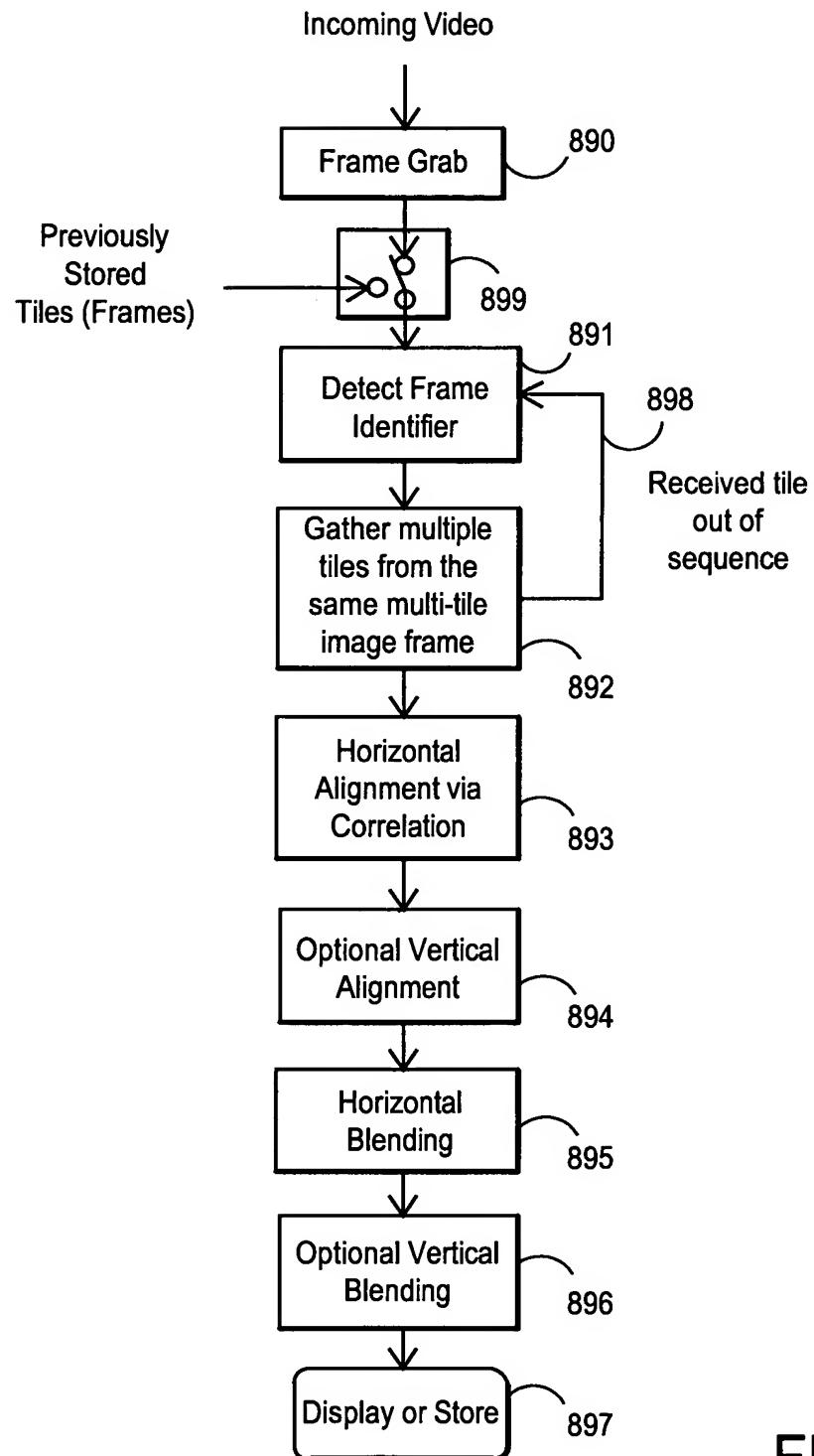


FIG. 13

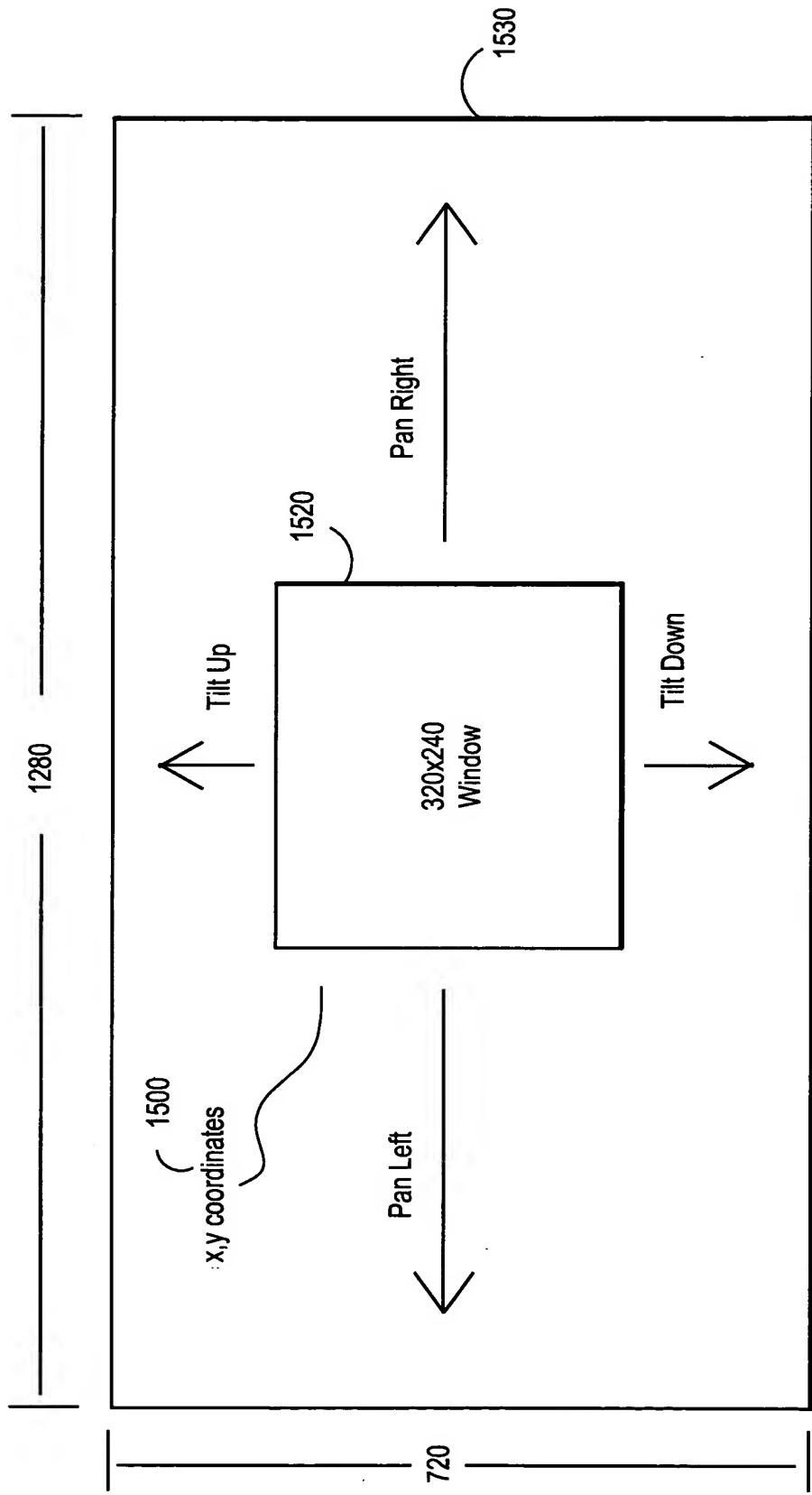


FIG. 14